

LM809/LM810

3-Pin Microprocessor Reset Circuits

General Description

The LM809/810 microprocessor supervisory circuits can be used to monitor the power supplies in microprocessor and digital systems. They provide a reset to the microprocessor during power-up, power-down and brown-out conditions.

The function of the LM809/810 is to monitor the $V_{\rm CC}$ supply voltage, and assert a reset signal whenever this voltage declines below the factory-programmed reset threshold. The reset signal remains asserted for 240ms after $V_{\rm CC}$ rises above the threshold. The LM809 has an active-low RESET output, while the LM810 has an active-high RESET output. Seven standard reset voltage options are available, suitable for monitoring 5V, 3.3V, and 3V supply voltages.

With a low supply current of only $15\mu A$, the LM809/810 are ideal for use in portable equipment. The LM809/LM810 are available in the 3-pin SOT23 package and in the 6-Lead LLP package.

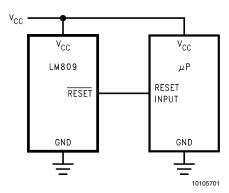
Features

- Precise monitoring of 3V, 3.3V, and 5V supply voltages
- Superior upgrade to MAX809/810
- Fully specified over temperature
- 140ms min. Power-On Reset pulse width, 240ms typical Active-low RESET Output (LM809)
 Active-high RESET Output (LM810)
- Guaranteed RESET Output valid for V_{CC}≥1V
- Low Supply Current, 15µA typ.
- Power supply transient immunity

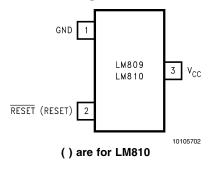
Applications

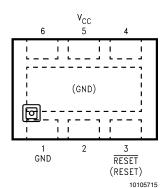
- Microprocessor Systems
- Computers
- Controllers
- Intelligent Instruments
- Portable/Battery-Powered Equipment
- Automotive

Typical Application Circuit



Connection Diagrams





Top View
See NS package Number LDB06A
() are for LM810

Ordering Information

Reset Threshold	LM809 Supplied as 1000	LM809 Supplied as 3000	Package	Package		
		• •	•		NCC Dealsons	
(V)	units, tape & reel	units, tape & reel	Top Mark	Туре	NSC Package	
4.63	LM809M3-4.63	LM809M3X-4.63	S8B			
4.38	LM809M3-4.38	LM809M3X-4.38	S7B			
4.00	LM809M3-4.00	LM809M3X-4.00	S6B]		
3.08	LM809M3-3.08	LM809M3X-3.08	S5B	SOT23-3	M03B	
2.93	LM809M3-2.93	LM809M3X-2.93	S4B]		
2.63	LM809M3-2.63	LM809M3X-2.63	S3B]		
2.45	LM809M3-2.45	LM809M3X-2.45	SFB			
Reset Threshold	LM810 Supplied as 1000	LM810 Supplied as 3000	Package	Package		
(V)	units, tape & reel	units, tape & reel	Top Mark	Туре	NSC Package	
4.63	LM810M3-4.63	LM810M3X-4.63	SEB			
4.38	LM810M3-4.38	LM810M3X-4.38	SDB	1	MOOD	
4.00	LM810M3-4.00	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	000	1		
	LIVIO 1 01VI3-4.00	LM810M3X-4.00	SCB	COTOO	MOOD	
3.08	LM810M3-3.08	LM810M3X-4.00 LM810M3X-3.08	SBB	- SOT23-3	M03B	
3.08 2.93				SOT23-3	M03B	
	LM810M3-3.08	LM810M3X-3.08	SBB	SOT23-3	M03B	
2.93	LM810M3-3.08 LM810M3-2.93	LM810M3X-3.08 LM810M3X-2.93	SBB SAB	SOT23-3 Package	M03B	
2.93 2.63	LM810M3-3.08 LM810M3-2.93 LM810M3-2.63	LM810M3X-3.08 LM810M3X-2.93 LM810M3X-2.63	SBB SAB S9B		M03B NSC Package	

Custom voltages and improved accuracies are available, subject to minimum orders. Contact your local National Semiconductor Sales Office for information.

Pin Descriptions

PIN		NAME	FUNCTION		
(LLP)			FUNCTION		
1	1	GND	Ground reference		
3	2	RESET (LM809)	Active-low output. $\overline{\text{RESET}}$ remains low while V_{CC} is below the reset threshold, and for 240ms after V_{CC} rises above the reset threshold.		
	2	RESET (LM810)	Active-high output. RESET remains high while $V_{\rm CC}$ is below the reset threshold, and for 240ms after $V_{\rm CC}$ rises above the reset threshold.		
5	3	V _{CC}	Supply Voltage (+5V, +3.3V, or +3.0V)		

320mW

125°C

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

LLP-6 152°C/W -0.3V to 6.0V SOT23-3 326°C/W RESET, RESET -0.3V to $(V_{CC} + 0.3V)$ Ambient Temperature Range -40°C to +105°C Input Current, V_{CC} Pin 20mA Maximum Junction Temperature Output Current, RESET, RESET -65°C to +160°C Storage Temperature Range Pin 20mA Lead Temperature (soldering, Rate of Rise, V_{CC} 100V/µs 10sec) +300°C

Continuous Power Dissipation

(Note 4)

 θ_{JA} :

Electrical Characteristics

ESD Rating (Note 2)

 V_{CC} = full range, T_A = -40°C to +105°C, unless otherwise noted. Typical values are at T_A = +25°C, V_{CC} = 5V for 4.63/4.38/4.00 versions, V_{CC} = 3.3V for 3.08/2.93 versions, and V_{CC} = 3V for 2.63/2.45 version (Note 3).

2kV

Symbol	Parameter	ter Conditions		Min	Тур	Max	Unit
	V Pango	$T_A = 0^{\circ}C \text{ to } +70^{\circ}C$ $T_A = -40^{\circ}C \text{ to } +105^{\circ}C$		1.0		5.5	V
	V _{CC} Range			1.2		5.5	1 V
I _{cc}	Supply Current	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	V _{CC} <5.5V, LM8 -4.63/4.38/4.00		18	60	
			V _{CC} <3.6V, LM8 -3.08/2.93/2.63/2.45		15	50	
		$T_A = +85^{\circ}C \text{ to} +105^{\circ}C$	V _{CC} <5.5V, LM8 -4.63/4.38/4.00			100	- μ <i>Α</i>
			V _{CC} <3.6V, LM8 -3.08/2.93/2.63/2.45			100	
		LM84.63	$T_A = +25^{\circ}C$	4.56	4.63	4.70	V
			$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$	4.50		4.75	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	4.40		4.86	
			$T_A = +25^{\circ}C$	4.31	4.38	4.45	
	Reset Threshold (Note 5)	LM84.38	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.25		4.50	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	4.16		4.56	
		LM84.00	$T_A = +25^{\circ}C$	3.93	4.00	4.06	
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.89		4.10	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	3.80		4.20	
		LM83.08	$T_A = +25^{\circ}C$	3.04	3.08	3.11	
V_{TH}			$T_A = -40^{\circ} \text{C to } +85^{\circ} \text{C}$	3.00		3.15	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.92		3.23	
		LM82.93	T _A = +25°C	2.89	2.93	2.96	
			$T_A = -40^{\circ} \text{C to } +85^{\circ} \text{C}$	2.85		3.00	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.78		3.08	
		LM82.63	$T_A = +25^{\circ}C$	2.59	2.63	2.66	
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.55		2.70	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.50		2.76	
		LM82.45	$T_A = +25^{\circ}C$	2.41	2.45	2.49	
			$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$	2.38		2.52	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.33		2.57	
l l	Reset Threshold Temperature Coefficient		•		30		ppm
	V _{CC} to Reset Delay (Note 5)	$V_{CC} = V_{TH}$ to (V_{T})	_H – 100mV)		20		μ
		$T_A = -40^{\circ}C \text{ to } +8$		140	240	560	
	Reset Active Timeout Period	$T_A = +85^{\circ}C \text{ to } +1$		100		840	m

Electrical Characteristics (Continued)

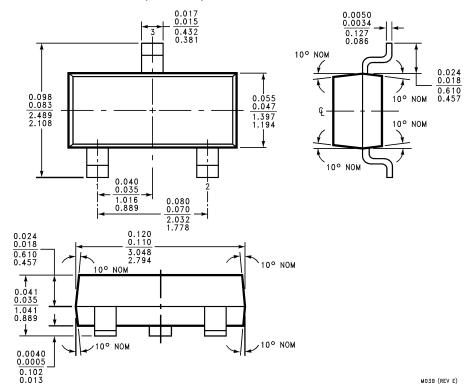
 $V_{\rm CC}$ = full range, T_A = -40°C to +105°C, unless otherwise noted. Typical values are at T_A = +25°C, $V_{\rm CC}$ = 5V for 4.63/4.38/4.00 versions, $V_{\rm CC}$ = 3.3V for 3.08/2.93 versions, and $V_{\rm CC}$ = 3V for 2.63/2.45 version (Note 3).

Symbol	Parameter	Conditions	Min	Тур	Max	Units	
V _{OL}	RESET Output Voltage Low (LM809)	V _{CC} = V _{TH} min, I _{SINK} = 1.2mA, LM809-2.45/2.63/2.93/3.08			0.3		
		$V_{CC} = V_{TH} \text{ min, } I_{SINK} = 3.2\text{mA}, \\ LM809-4.63/4.38/4.00$			0.4	V	
		$V_{CC} > 1.0V, I_{SINK} = 50\mu A$			0.3		
V _{OH}	RESET Output Voltage High (LM809)	$V_{CC} > V_{TH} \text{ max}, I_{SOURCE} = 500\mu\text{A},$ LM809-2.45/2.63/2.93/3.08	0.8V _{CC}			V	
		$V_{CC} > V_{TH} \text{ max}, I_{SOURCE} = 800 \mu A, \\ LM809-4.63/4.38/4.00$	V _{CC} -1.5				
V _{OL}	RESET Output Voltage Low (LM810)	V _{CC} = V _{TH} max, I _{SINK} = 1.2mA, LM810-2.63/2.93/3.08			0.3	V	
		V _{CC} = V _{TH} max, I _{SINK} = 3.2mA, LM810-4.63/4.38/4.00			0.4		
V _{OH}	RESET Output Voltage High (LM810)	$1.8V < V_{CC} < V_{TH} min, I_{SOURCE} = 150 \mu A$	0.8V _{CC}			V	

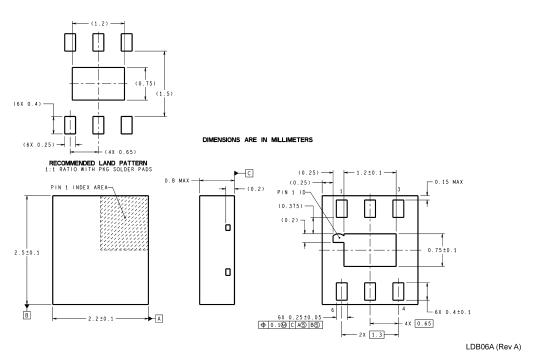
Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Ratings are conditions under which the device operates correctly. Operating ratings do not imply guaranteed performance limits. For guaranteed performance limits and associated test conditions, see the Electrical Characteristics.

- Note 2: The human body model is a 100pF capacitor discharged through a $1.5k\Omega$ resistor into each pin.
- **Note 3:** Production testing done at $T_A = +25^{\circ}C$, over temperature limits guaranteed by design only.
- Note 4: At elevated temperatures, devices must be derated based on package thermal resistance. The device in the SOT23-3 package must be derated at 4mW/°C at ambient temperatures above 70°C. The device has internal thermal protection.
- Note 5: RESET Output for LM809, RESET output for LM810.

Physical Dimensions inches (millimeters) unless otherwise noted



3-Lead SOT23-3
For Ordering, refer to Ordering Information table
NS Package Number M03B



6-Lead LLP (NLDBB006)
For Ordering, refer to Ordering Information table
NS Package Number LDB06A